

FIG.1

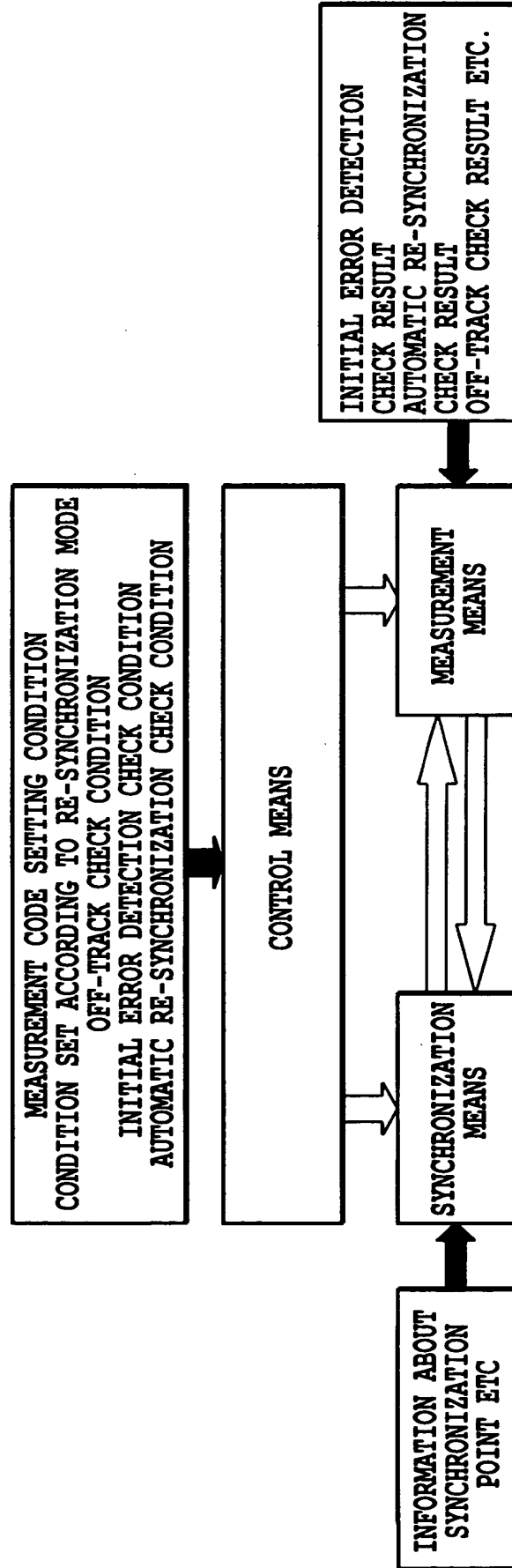


FIG.2

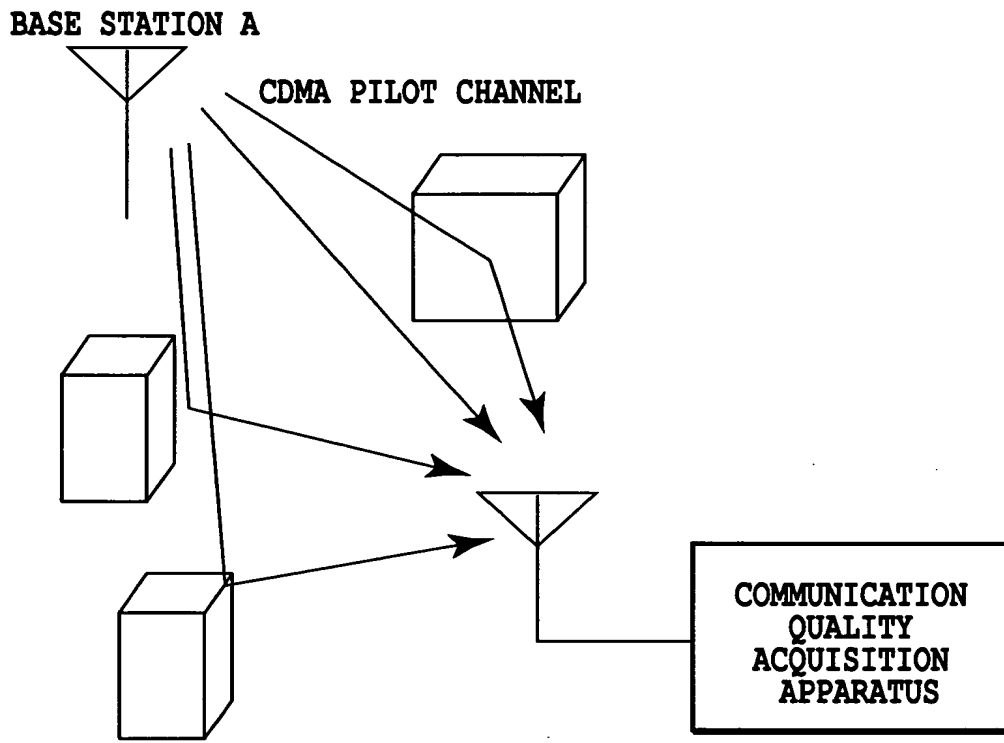


FIG.3A

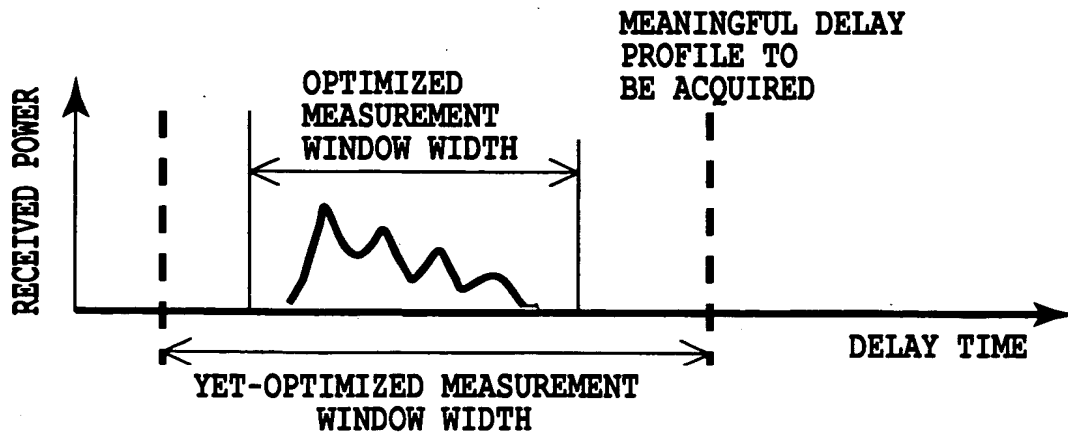


FIG.3B

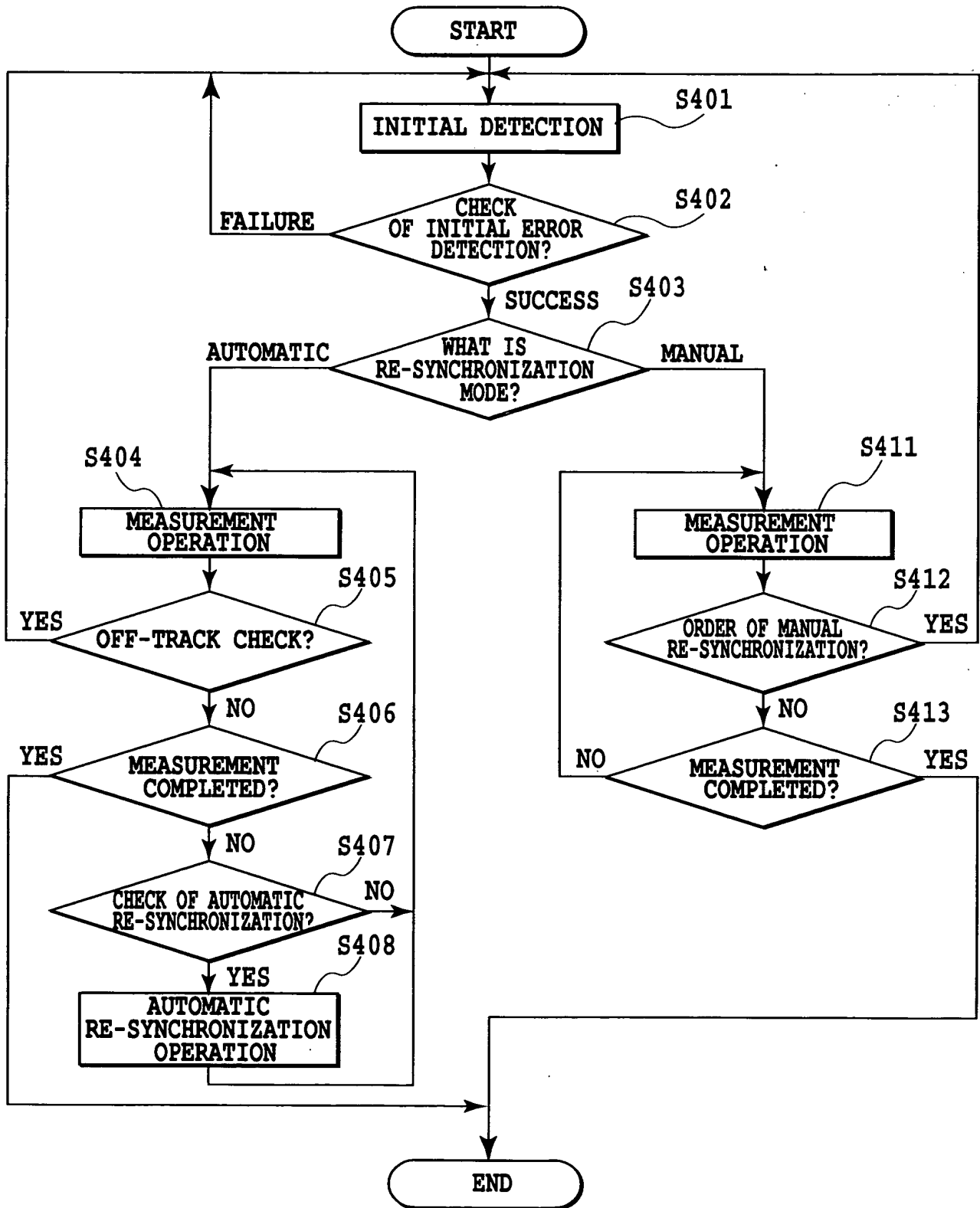
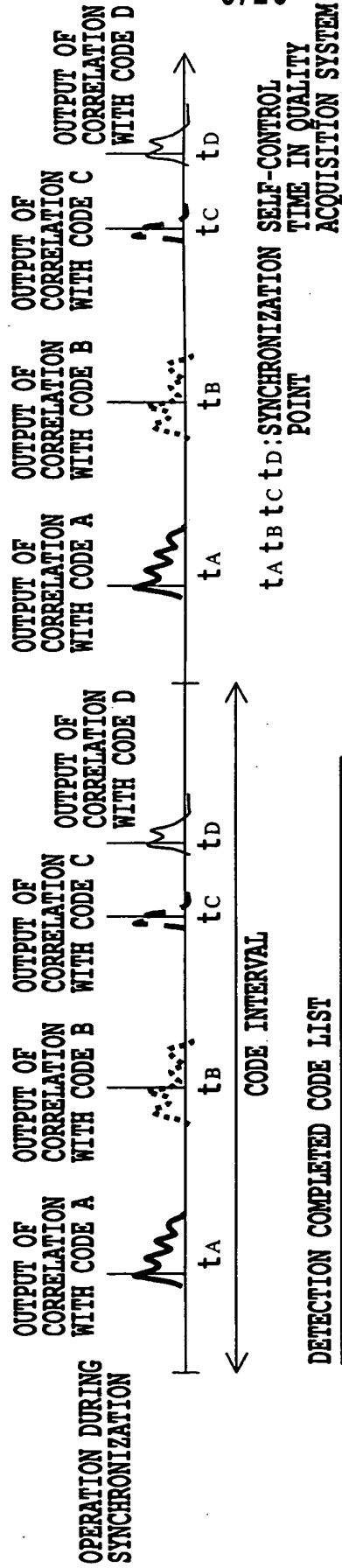


FIG.4

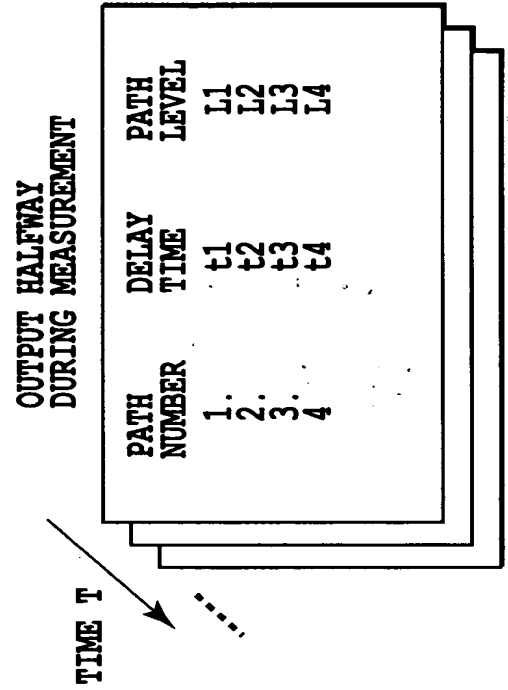
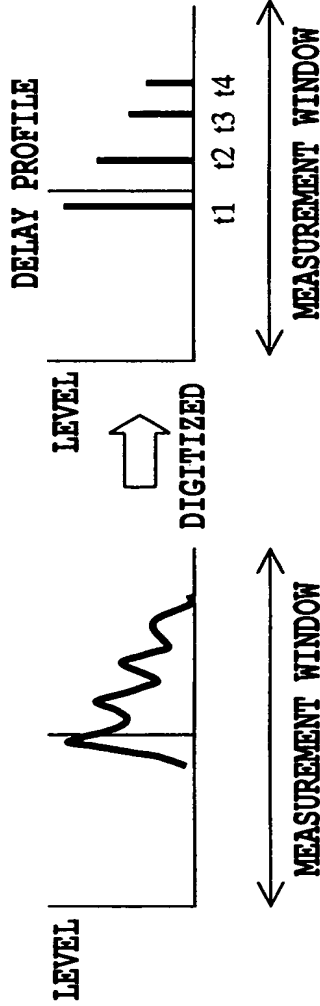
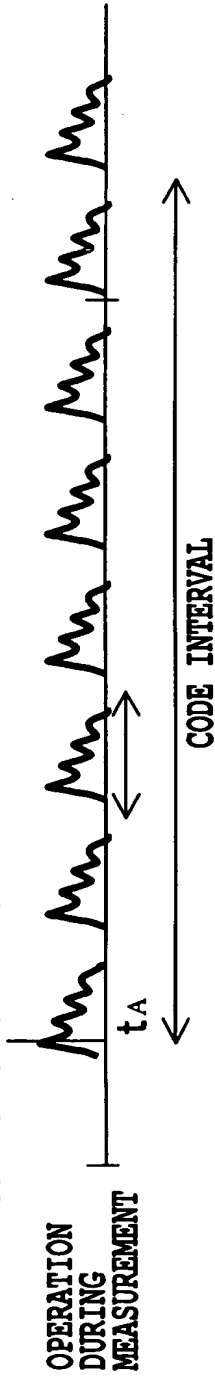


CODE NUMBER	CODE NUMBER	CODE NUMBER
1. A	BASE STATION A	$t_A$
2. B	BASE STATION B	$t_B$
3. C	BASE STATION C	$t_C$
4. D	BASE STATION D	$t_D$

OUTPUT HALFWAY DURING SYNCHRONIZATION

FIG.5

OUTPUT OF CORRELATION  
WITH CODE A AFTER THE  
ESTABLISHMENT OF  
CODE SYNCHRONIZATION



PATH NUMBER	DELAY TIME	PATH LEVEL
1.	$t_1$	L1
2.	$t_2$	L2
3.	$t_3$	L3
4	$t_4$	L4

FIG.6

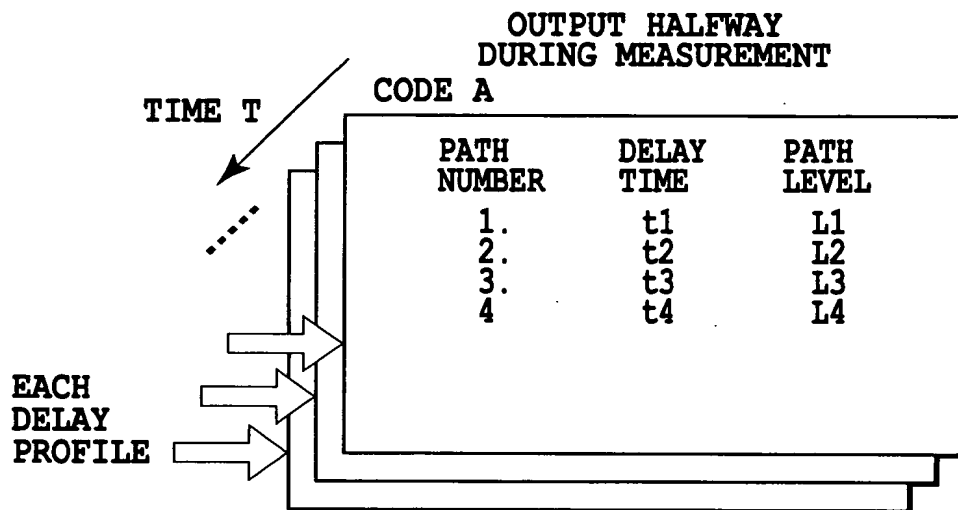


FIG.7A

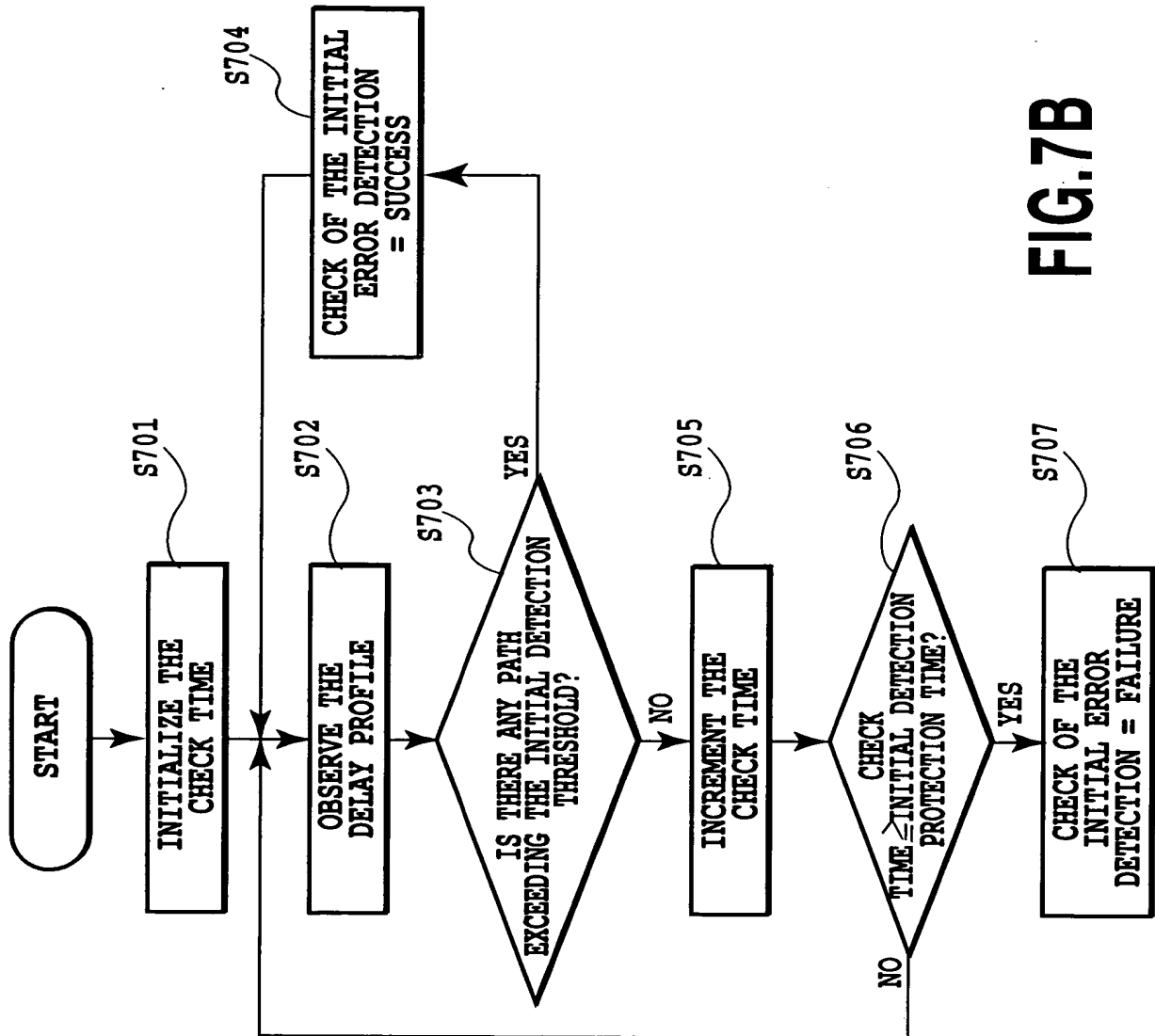


FIG. 7B



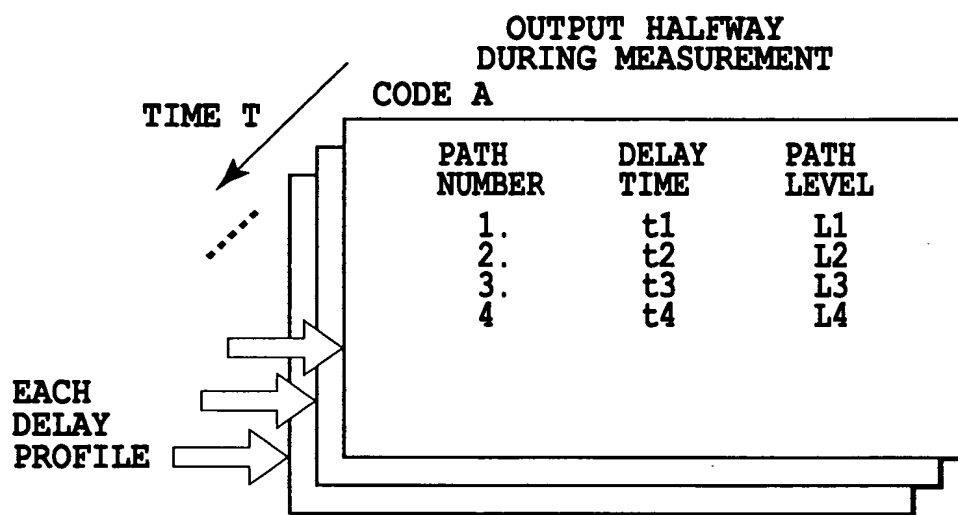


FIG.8A

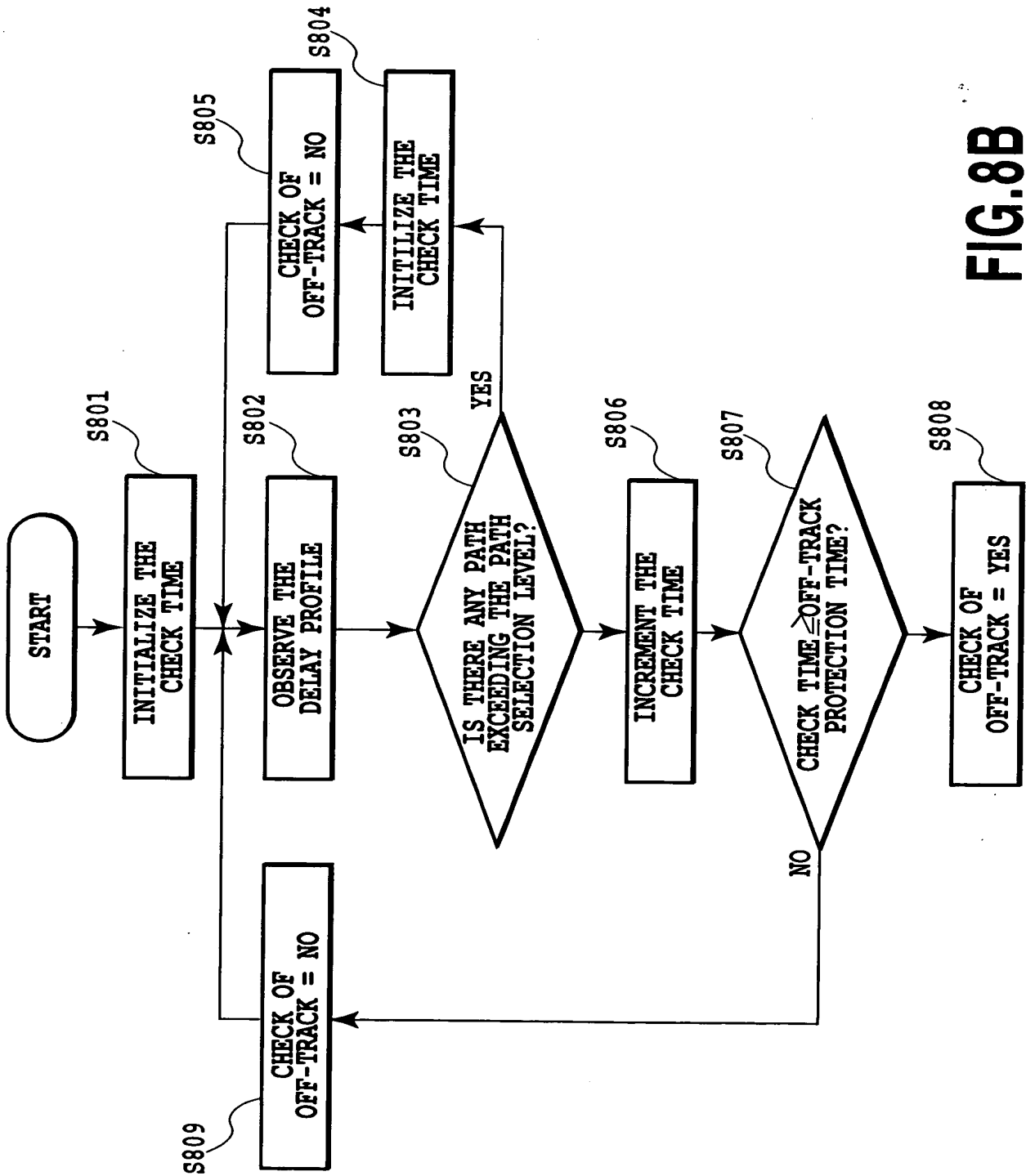


FIG. 8B

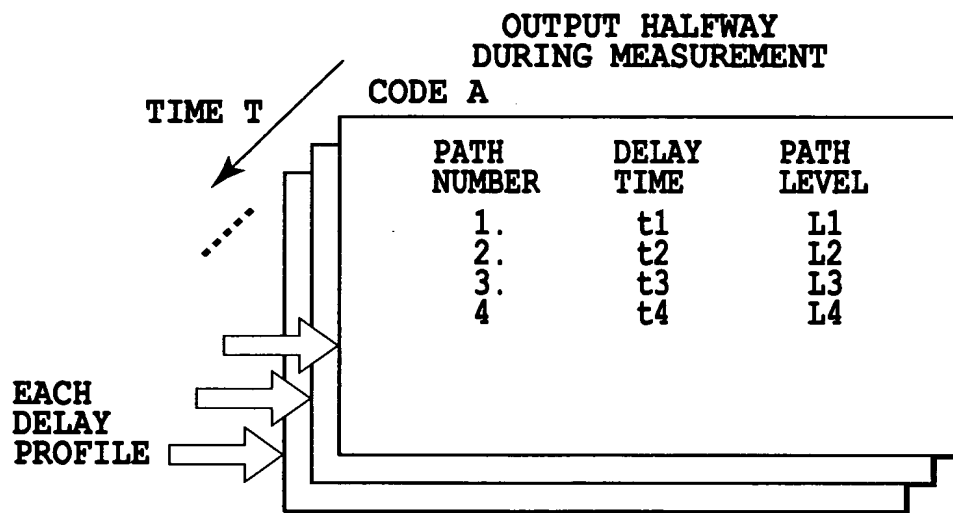


FIG.9A

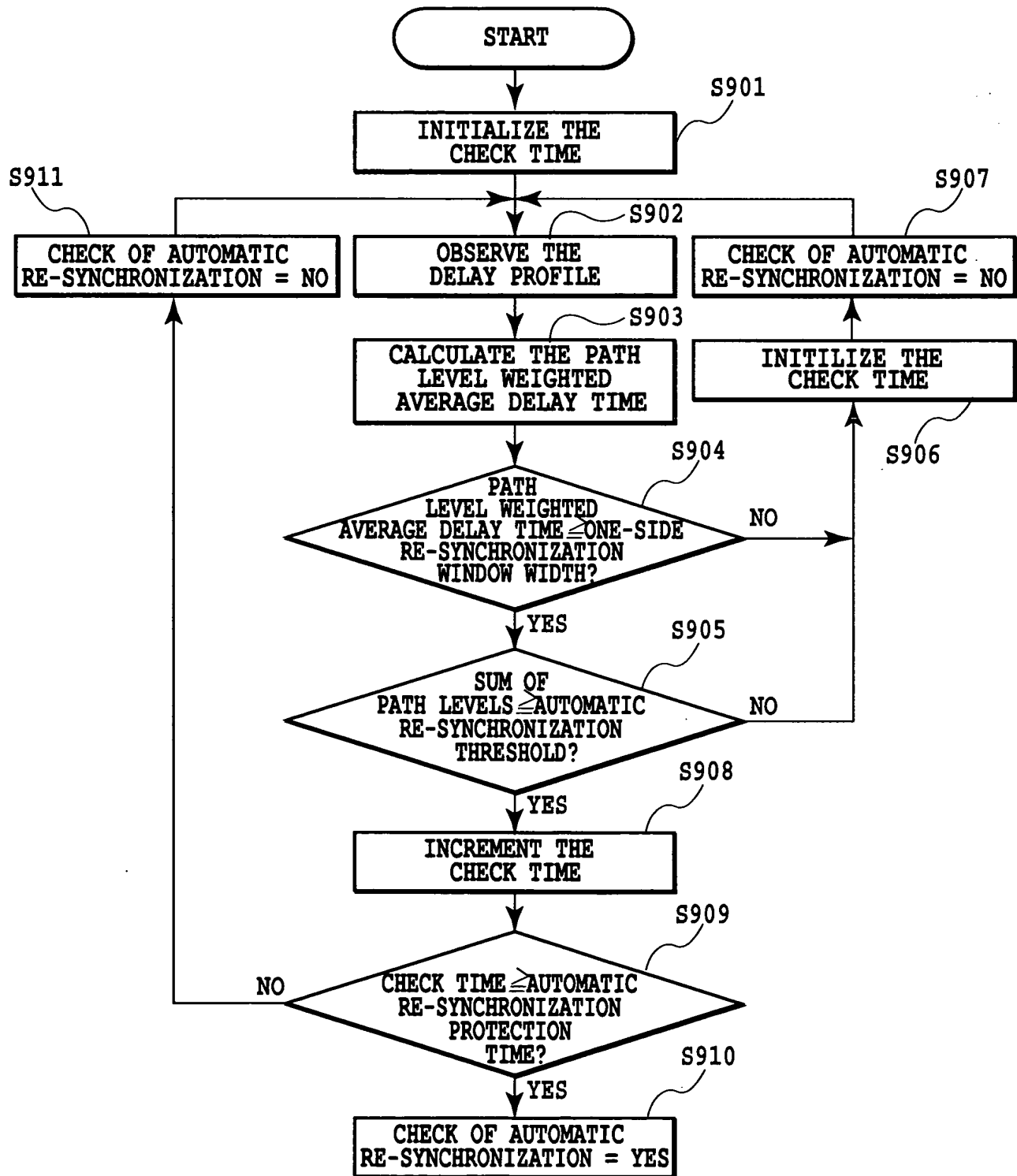


FIG.9B

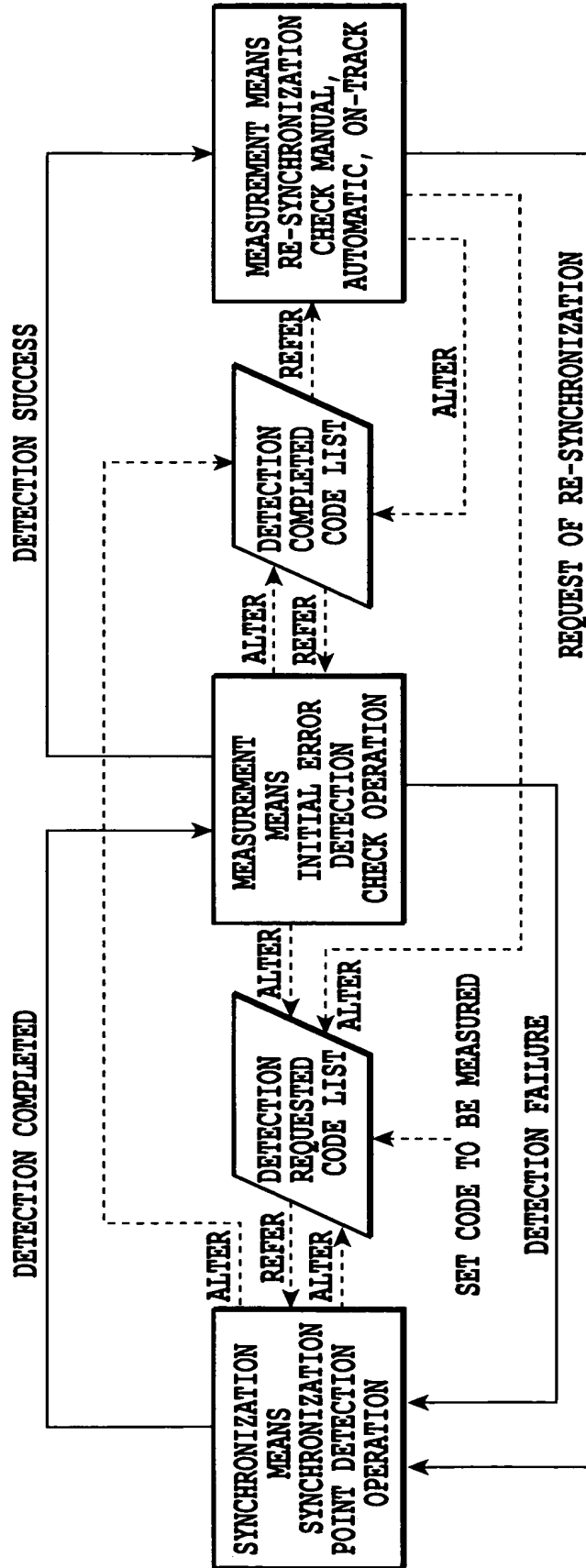


FIG.10

SET CODE TO  
BE MEASURED

SEARCH NUMBER	CODE NUMBER	NAME OF BASE STATION
1.	3	BASE STATION A
2.	6	BASE STATION B
3.	120	BASE STATION C
4.	55	BASE STATION D
5.	412	BASE STATION E
6.	501	BASE STATION F
7.	9	BASE STATION G
8.	378	BASE STATION H

FIG.11A

DETECTION REQUESTED  
CODE LIST

SEARCH NUMBER	CODE NUMBER	NAME OF BASE STATION
1.	3	BASE STATION A
2.	6	BASE STATION B
3.	120	BASE STATION C
4.	55	BASE STATION D
5.	412	BASE STATION E
6.	501	BASE STATION F
7.	9	BASE STATION G
8.	378	BASE STATION H

FIG.11B

DETECTION REQUESTED  
CODE LIST

SEARCH NUMBER	CODE NUMBER	NAME OF BASE STATION
1.	3	BASE STATION A
2.	6	BASE STATION B
3.	120	BASE STATION C
4.	55	BASE STATION D
5.	412	BASE STATION E
6.	501	BASE STATION F
7.	9	BASE STATION G
8.	378	BASE STATION H

FIG.11C

EXAMPLE OF COMPLETED  
DETECTION FOR CODE-3 AND CODE-6

DETECTION COMPLETED  
CODE LIST

SEARCH NUMBER	CODE NUMBER	NAME OF BASE STATION	SYNCHRO- NIZATION POINT
1.	3	BASE STATION A	1206
2.	6	BASE STATION B	408
3.			
4.			
5.			
6.			
7.			
8.			

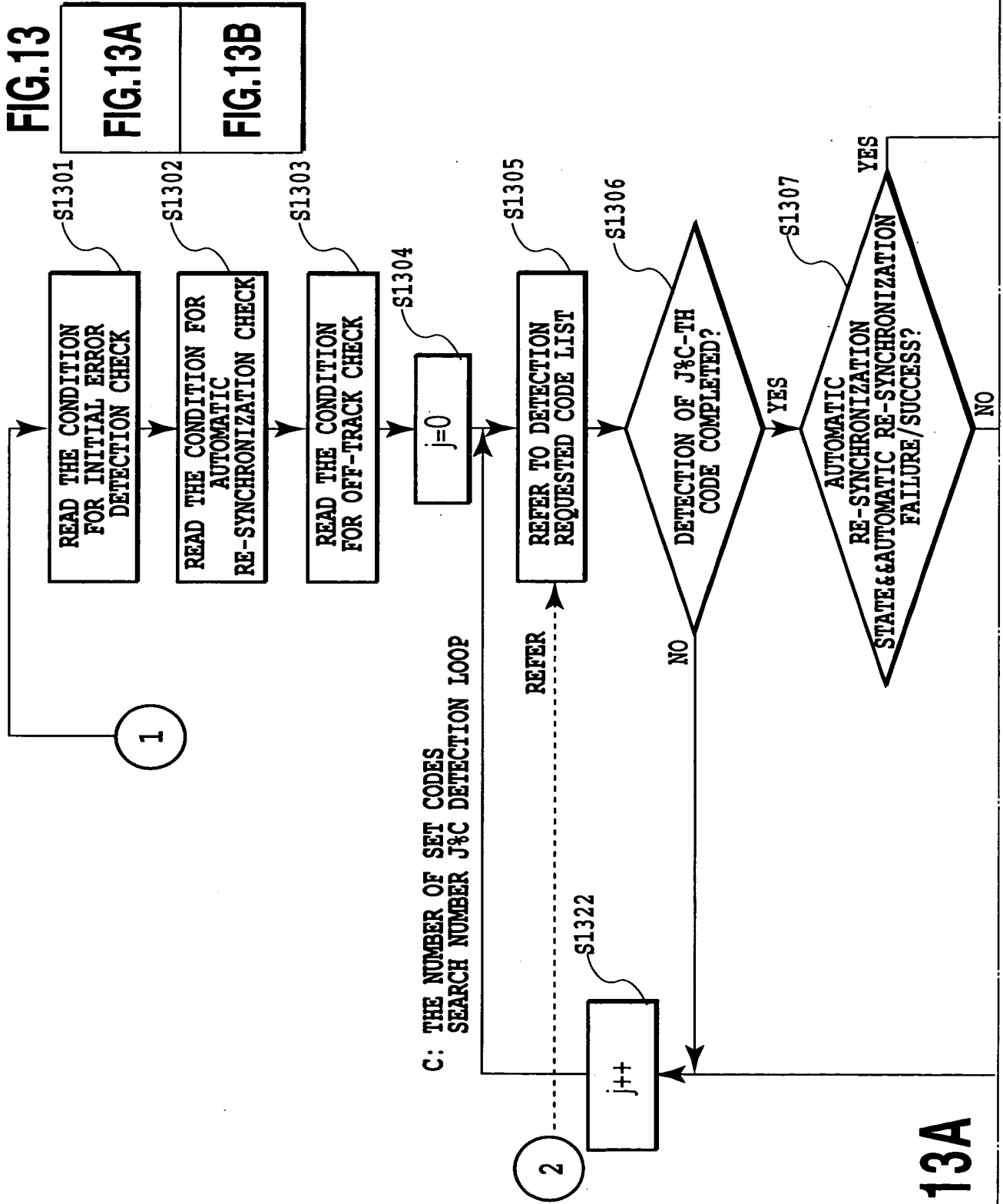
FIG.11D







FIG.13



C: THE NUMBER OF SET CODES  
SEARCH NUMBER J&C DETECTION LOOP

FIG.13A

FIG. 13B

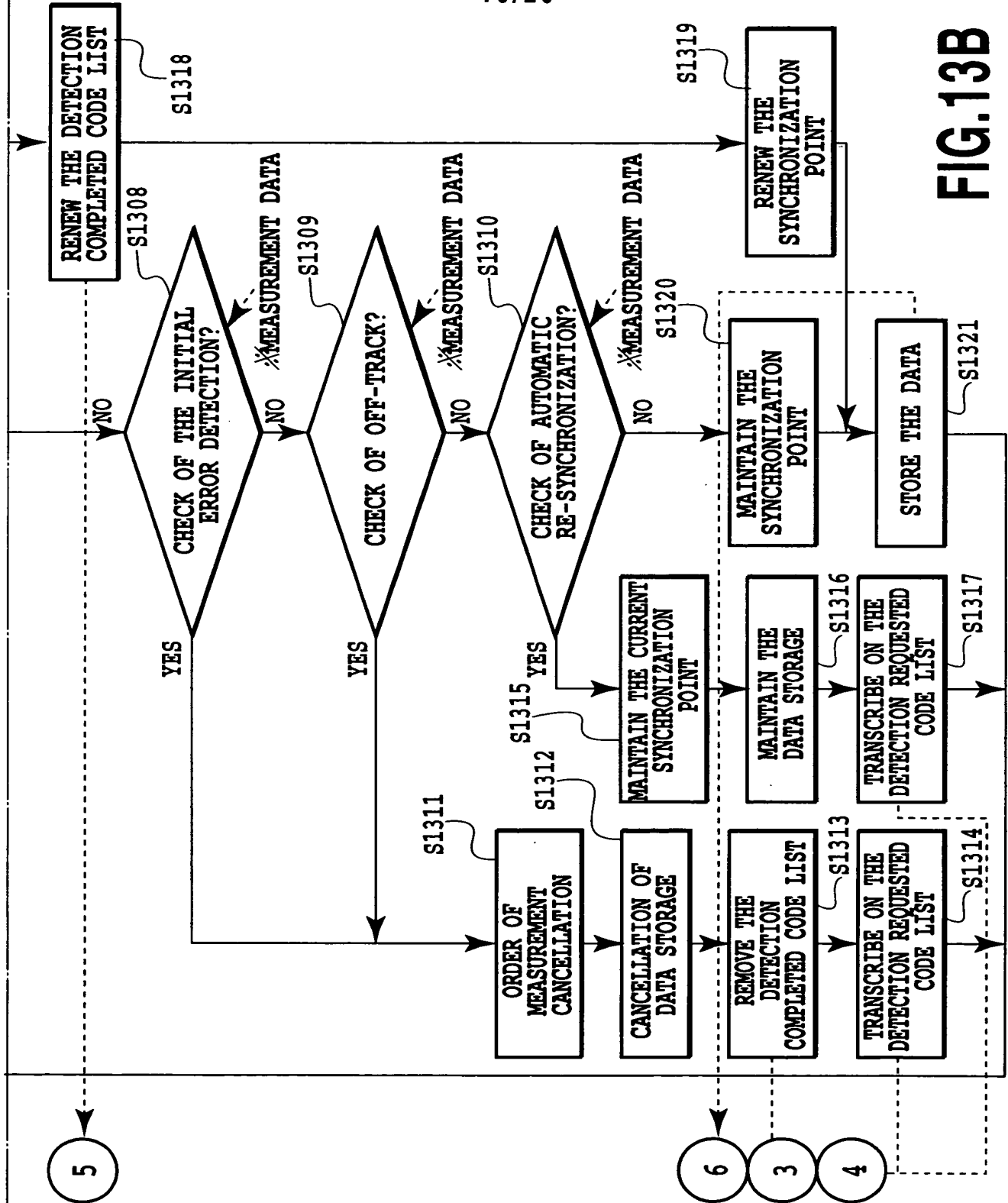


FIG.14

FIG.14A

FIG.14B

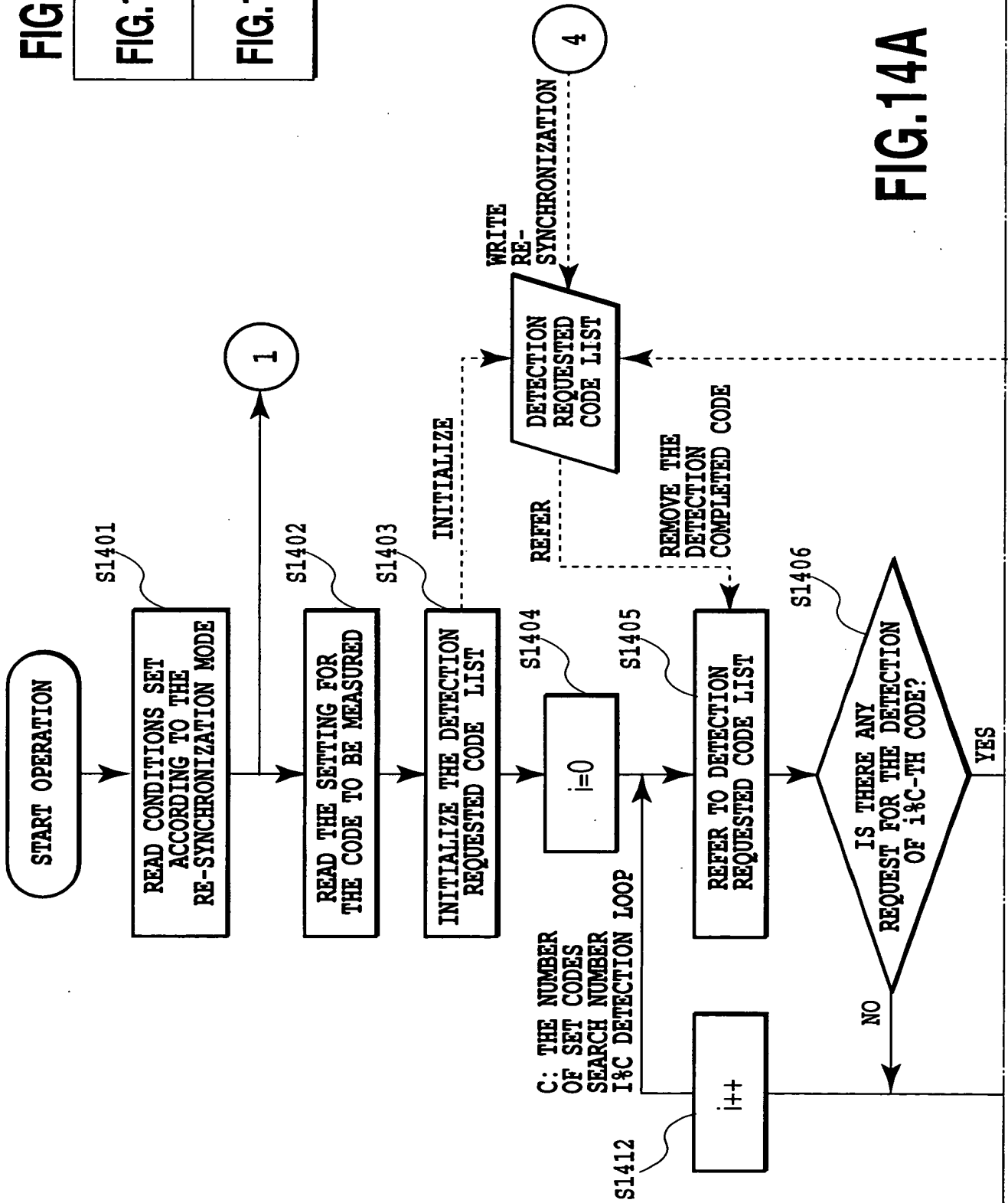


FIG.14A

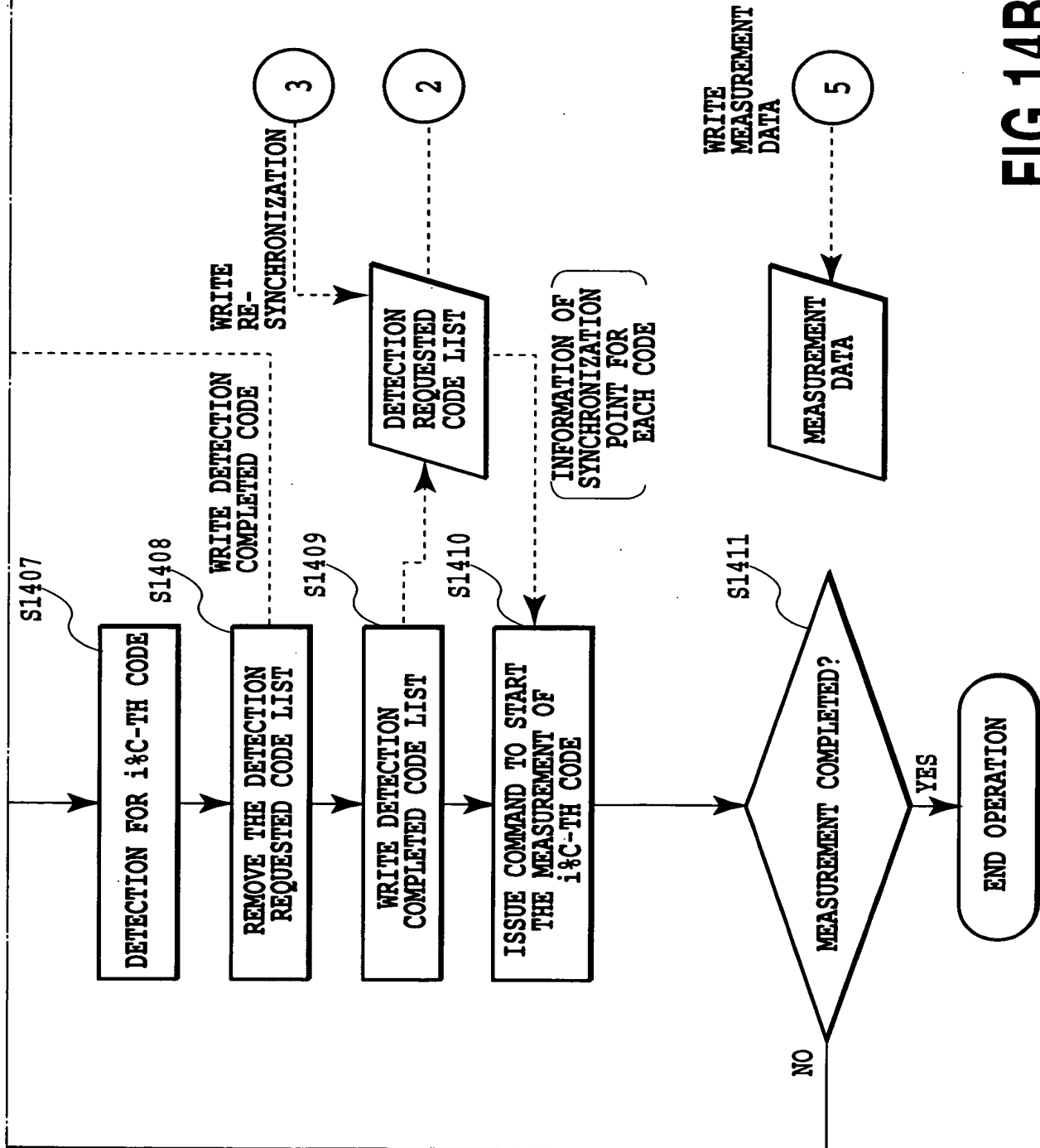


FIG.14B

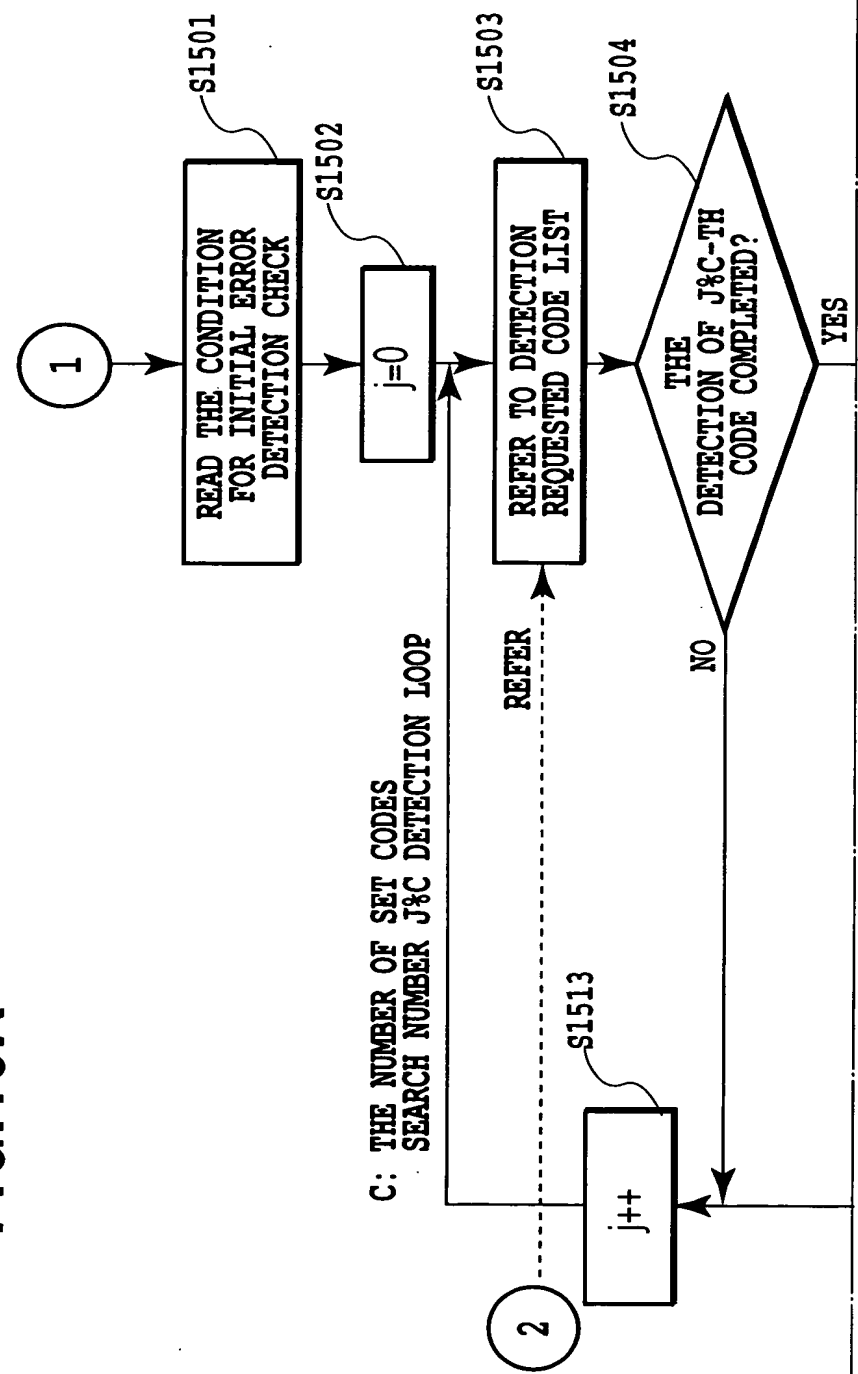
FIG. 15A is a flowchart illustrating a process for detecting an initial error condition. The process begins at step 1, where the condition for an initial error detection check is read (S1501). This is followed by setting a counter j to 0 (S1502). The process then enters a loop where it refers to the detection requested code list (S1503) and checks if the detection of the j&C-th code is completed (S1504). If the detection is completed (YES), the process proceeds to step 2. If not completed (NO), the counter j is incremented (S1513) and the process loops back to referring to the detection requested code list (S1503).

FIG.15

FIG.15A

FIG.15B

FIG.15A



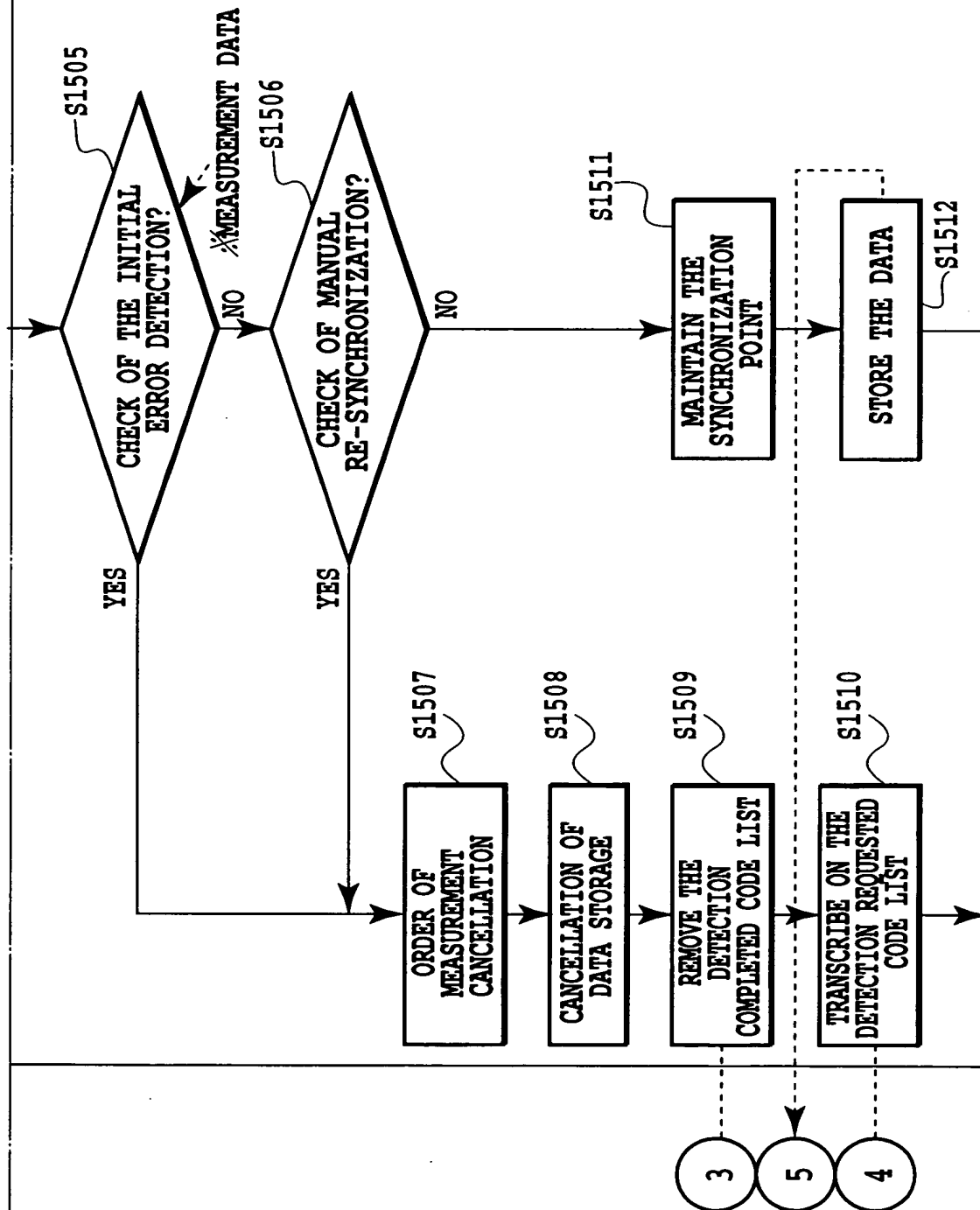


FIG. 15B

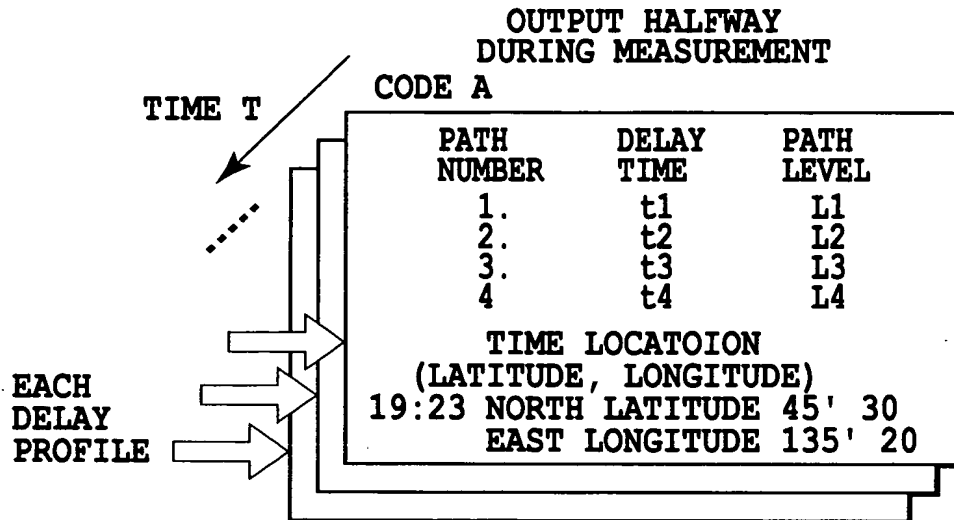


FIG.16A

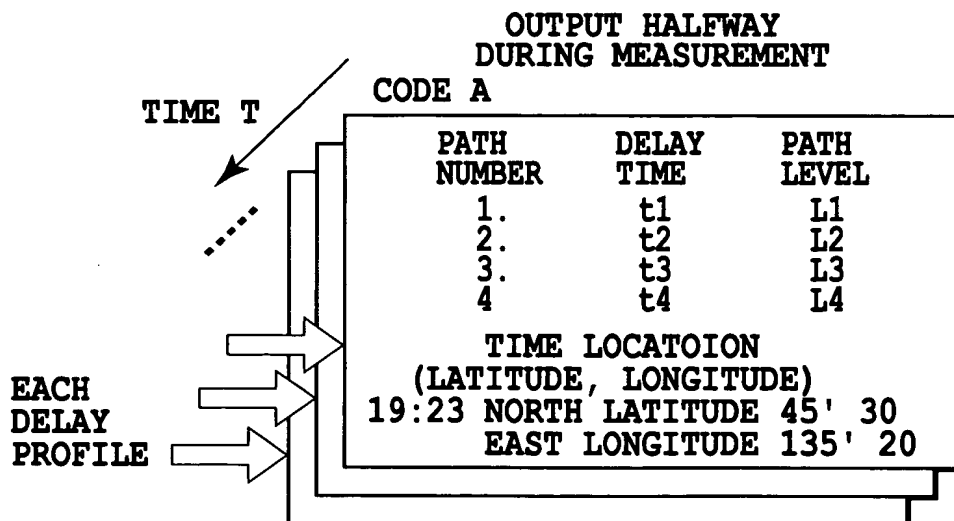


FIG.16B